

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Pegler, P.)	Examiner: Quinto, K.
)	
Serial No.: 10/278,303)	Art Unit: 2826
)	
Filed: October 22, 2002)	
)	
For: STRUCTURE FOR A JUNCTION)	
FIELD EFFECT TRANSISTOR)	
WITH REDUCED GATE)	
CAPACITANCE)	
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT FOR DIVISIONAL

Dear Sir:

As a preliminary amendment for the present Divisional,
Applicant respectfully requests the Examiner to consider the
following amendments.